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Vishay General Semiconductor

# **SMD** Photovoltaic Solar Cell Protection Rectifier



DO-214AB (SMC)

#### **PRIMARY CHARACTERISTICS** 5.0 A I<sub>F(AV)</sub> V<sub>RRM</sub> 1000 V 100 A IFSM $I_R$ 10 µA $V_F$ at $I_F = 5.0$ A 0.90 V 150 °C T<sub>J</sub> max. DO-214AB (SMC) Package Single die **Diode variations**

## FEATURES

- Low profile package
- · Ideal for automated placement
- · Glass passivated pellet chip junction
- Low forward voltage drop
- · Low leakage current
- · High forward surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

## **TYPICAL APPLICATIONS**

For use in solar cell panel blocking diode for protection, using DC forward current without reverse bias.

## **MECHANICAL DATA**

**Case:** DO-214AB (SMC) Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade

**Terminals:** Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

Polarity: Color band denotes cathode end

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	S5MS	UNIT	
Device marking code			5MS		
Max. repetitive peak reverse voltage		V <sub>RRM</sub>	1000	V	
Max. DC forward current (fig. 1)	T <sub>M</sub> = 110 °C	I <sub>F</sub>	5.0 (1)	A	
	T <sub>A</sub> = 25 °C		1.6 <sup>(2)</sup>		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub> 100		А	
Operating junction and storage temperature range		T <sub>OP</sub> , T <sub>STG</sub>	-55 to +150	°C	
Junction temperature in DC forward current without reverse bias, t $\leq$ 1 h $^{(3)}$		TJ	≤ 200	°C	

### Notes

<sup>(1)</sup> Mounted on 30 mm x 30 mm AI PCB

<sup>(2)</sup> Free air, mounted on recommended copper pad area

<sup>(3)</sup> Meets the requirements of IEC 61215 Ed. 2 bypass diode thermal test

(e3) RoHS www.vishay.com

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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25$ °C unless otherwise noted)						
PARAMETER	TEST CO	ONDITIONS	SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 2.5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.94	-	V
	I <sub>F</sub> = 5.0 A			0.99	1.15	
	I <sub>F</sub> = 2.5 A	T <sub>A</sub> = 125 °C		0.82	-	
	I <sub>F</sub> = 5.0 A			0.90	1.00	
Reverse current	Rated V <sub>B</sub>	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	-	10	μA
	naleu v <sub>R</sub>	T <sub>A</sub> = 125 °C		50	250	
Max. reverse recovery time	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = I <sub>rr</sub> = 0.25 A	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{rr} = 0.25 \text{ A}$		2.5	-	μs
Typical junction capacitance	4.0 V, 1 MHz		CJ	40	-	pF

### Notes

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  40 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER	SYMBOL	S5MS	UNIT		
Typical thermal resistance	R <sub>0JA</sub> <sup>(1)</sup>	92	°C/W		
	R <sub>0JM</sub> <sup>(2)</sup>	8			

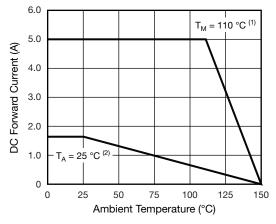
### Notes

<sup>(1)</sup> Free air, mounted on recommended copper pad area. Thermal resistance R<sub>0JA</sub> - junction to ambient

 $^{(2)}$  Mounted on 30 mm x 30 mm AI PCB. Thermal resistance  $R_{\theta JM}$  - junction to mount

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
S5MS-E3/57T	0.211	57T	850	7" diameter plastic tape and reel		
S5MS-E3/9AT	0.211	9AT	3500	13" diameter plastic tape and reel		

## RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)





### Notes

- $^{(1)}$  Mounted on 30 mm x 30 mm AI PCB  $T_M$  measured at the terminal (R\_{\theta JM} = 8  $^{\circ}C/W)$
- $^{(2)}$  Free air, mounted on recommended copper pad area (R $_{\rm 0JA}$  = 92 °C/W)

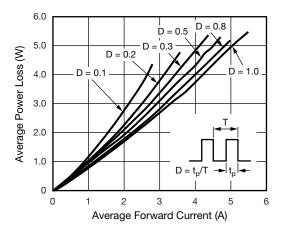


Fig. 2 - Forward Power Loss Characteristics

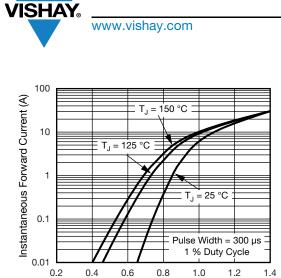
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Instantaneous Forward Voltage (V) Fig. 3 - Typical Instantaneous Forward Characteristics

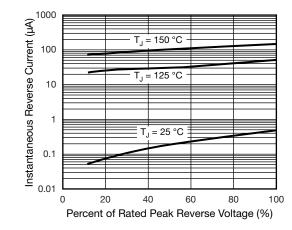
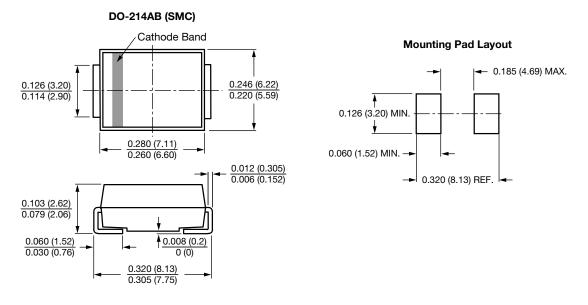


Fig. 4 - Typical Reverse Characteristics





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Fig. 5 - Typical Junction Capacitance

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