





100V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

| V _{(BR)DSS} | R _{DS(ON)} max | I _D max T _A = +25°C | | | |
|----------------------|-------------------------------|--|--|--|--|
| 100V | 110mΩ @ V _{GS} = 10V | 3.6A | | | |
| | 122mΩ @ V_{GS} = 6.0V | 3.4A | | | |

Description

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- DC-DC Converters
- Power Management Functions

Features and Benefits

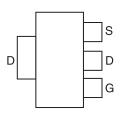
- Low On-Resistance
- Low Input Capacitance
- · Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

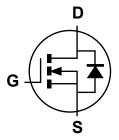
- Case: SOT223
- Case Material: Molded Plastic, "Green" Molding Compound;
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 ⁽³⁾
- Weight: 0.112 grams (Approximate)







Pin Out - Top View



Equivalent Circuit

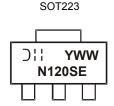
Ordering Information (Note 4)

| Part Number | Qualification | Case | Packaging |
|----------------|---------------|--------|-------------------|
| Fait Nullibei | Qualification | Case | rackayiiiy |
| DMN10H120SE-13 | Standard | SOT223 | 2,500/Tape & Reel |

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



O!! = Manufacturer's Marking N120SE = Marking Code YWW = Date Code Marking Y or Y= Year (ex: 5 = 2015) WW = Week (01 - 53)



| Characteristic | Symbol | Value | Units | | |
|---|-----------------|--|------------------|------------|---|
| Drain-Source Voltage | | | V _{DSS} | 100 | V |
| Gate-Source Voltage | | | V _{GSS} | ±20 | V |
| Continuous Drain Current (Note 6) V _{GS} = 10V | Steady State | T _A = +25°C T _A = +70°C | I _D | 3.6 2.9 | А |
| Pulsed Drain Current (10µs pulse, duty cycle ≦1%) | | | I _{DM} | 16 | Α |
| Maximum Body Diode Continuous Current (Note 6) | | | Is | 2.5 | Α |

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Value | Units | |
|---|----------|-----------------------------------|-------------|------|
| Total Power Dissipation | (Note 5) | | 1.3 | W |
| Total Power Dissipation | (Note 6) | P _D | 2.1 | |
| Thormal Basistanas, Junation to Ambient | (Note 5) | D | 94 | °C/W |
| Thermal Resistance, Junction to Ambient | (Note 6) | $R_{\theta JA}$ | 58 | |
| Thermal Resistance, Junction to Case | (Note 6) | $R_{\theta JC}$ | 8.2 | |
| Operating and Storage Temperature Range | | T _J , T _{STG} | -55 to +150 | °C |

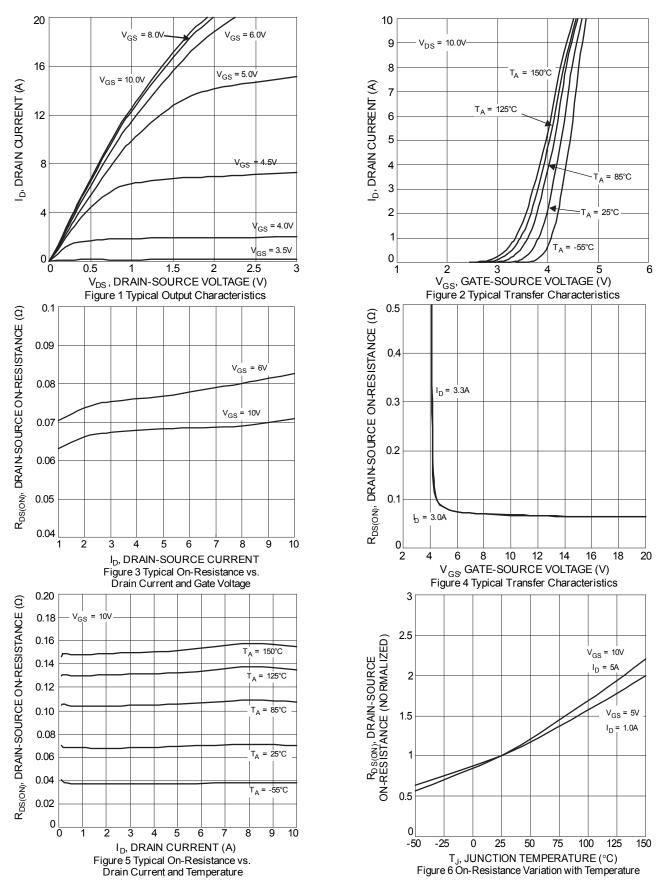
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition | |
|--|----------------------|-----|-----|------|-------|---|--|
| OFF CHARACTERISTICS (Note 7) | | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | 100 | _ | _ | V | V _{GS} = 0V, I _D = 250μA | |
| Zero Gate Voltage Drain Current | I _{DSS} | _ | _ | 1.0 | μA | V _{DS} = 80V, V _{GS} = 0V | |
| Gate-Body Leakage | I _{GSS} | _ | _ | ±100 | nA | V _{GS} = ±20V, V _{DS} = 0V | |
| ON CHARACTERISTICS (Note 7) | | | | | | | |
| Gate Threshold Voltage | V _{GS(th)} | 1.5 | 2.6 | 3.0 | V | $V_{DS} = V_{GS}, I_D = 250 \mu A$ | |
| Static Drain-Source On-Resistance | 0 | _ | 77 | 110 | mΟ | V _{GS} = 10V, I _D = 3.3A | |
| Static Drain-Source On-Resistance | R _{DS (ON)} | _ | 84 | 122 | 11122 | V _{GS} = 6.0V, I _D = 3.0A | |
| Diode Forward Voltage | V_{SD} | _ | 0.8 | 1.2 | V | V _{GS} = 0V, I _S = 3.2A | |
| DYNAMIC CHARACTERISTICS (Note 8) | · I. | I. | I. | I. | | | |
| Input Capacitance | C _{iss} | _ | 549 | _ | | V _{DS} = 50V, V _{GS} = 0V, f = 1.0MHz | |
| Output Capacitance | Coss | _ | 41 | _ | pF | | |
| Reverse Transfer Capacitance | C _{rss} | _ | 19 | _ | | | |
| Gate Resistance | Rg | _ | 1.6 | _ | Ω | VDS = 0V, VGS = 0V, f = 1.0MHz | |
| Total Gate Charge (V _{GS} = 10V) | Qg | _ | 10 | _ | | | |
| Total Gate Charge (V _{GS} = 4.5V) | Qg | _ | 5.2 | | | | |
| Gate-Source Charge | Q _{gs} | _ | 2.3 | _ | nC | $V_{DS} = 50V, I_D = 3.3A$ | |
| Gate-Drain Charge | Q _{gd} | _ | 2.6 | _ | | | |
| Turn-On Delay Time | t _{D(on)} | _ | 3.8 | _ | | | |
| Turn-On Rise Time | tr | _ | 1.8 | _ | | V _{DD} = 50V, V _{GS} = 10V, | |
| Turn-Off Delay Time | t _{D(off)} | _ | 11 | _ | nS | $R_G = 6.0\Omega$, $I_D = 3.3A$ | |
| Turn-Off Fall Time | t _f | _ | 2.5 | _ | | | |
| Reverse Recovery Time | t _{rr} | _ | 21 | _ | nS | \\ 0\\ -4 40 4\\ d\\ d\\ d\\ - | |
| Reverse Recovery Charge | Q _{rr} | _ | 17 | _ | nC | $V_{GS} = 0V$, $I_{S} = 1.1A$, $di/dt = 100A/\mu s$ | |

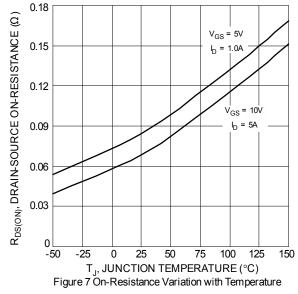
Notes:

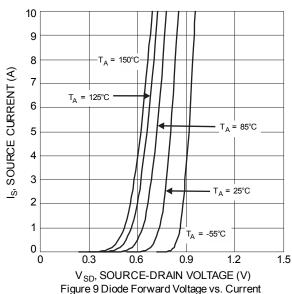
- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.

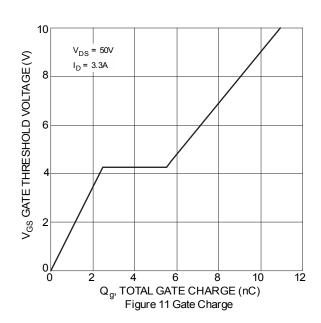


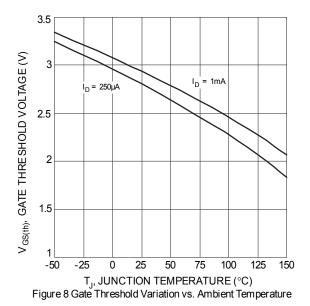


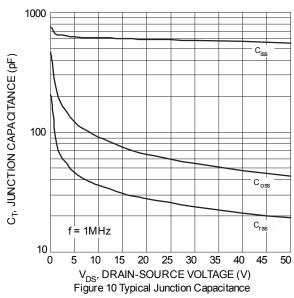


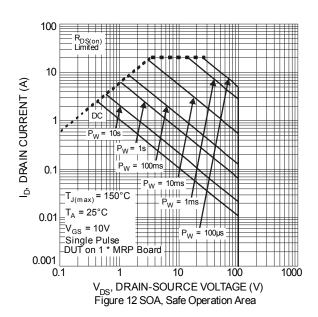




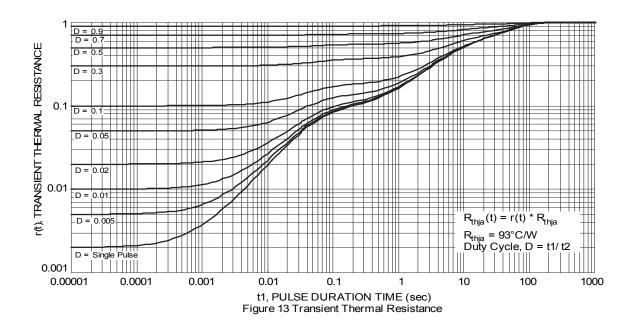






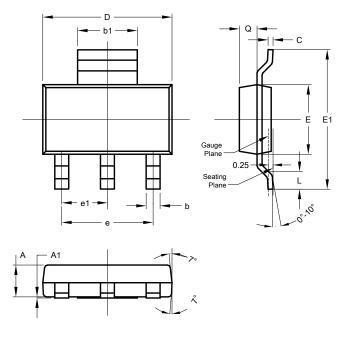






Package Outline Dimensions & Suggested Pad Layout

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

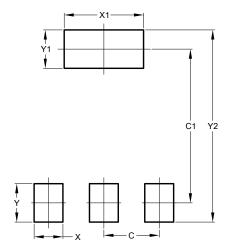


| SOT223 | | | | | |
|----------------------|-------|------|------|--|--|
| Dim | Min | Max | Тур | | |
| Α | 1.55 | 1.65 | 1.60 | | |
| A1 | 0.010 | 0.15 | 0.05 | | |
| b | 0.60 | 0.80 | 0.70 | | |
| b1 | 2.90 | 3.10 | 3.00 | | |
| С | 0.20 | 0.30 | 0.25 | | |
| D | 6.45 | 6.55 | 6.50 | | |
| Е | 3.45 | 3.55 | 3.50 | | |
| E1 | 6.90 | 7.10 | 7.00 | | |
| е | - | - | 4.60 | | |
| e1 | - | - | 2.30 | | |
| L | 0.85 | 1.05 | 0.95 | | |
| Ø | 0.84 | 0.94 | 0.89 | | |
| All Dimensions in mm | | | | | |



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



| Dimensions | Value (in mm) | | | |
|------------|---------------|--|--|--|
| С | 2.30 | | | |
| C1 | 6.40 | | | |
| Х | 1.20 | | | |
| X1 | 3.30 | | | |
| Υ | 1.60 | | | |
| Y1 | 1.60 | | | |
| C2 | 8.00 | | | |

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