



4 CHANNEL LOW CAPACITANCE TVS DIODE ARRAY

Product Summary

V _{BR} (Min)	I _{PP} (Max)	C _T (Typ)
4.5V	20A	2.4pF

Description

The D5V0P4URL6SO is a high performance device suitable for protecting four high-speed I/Os. These devices are assembled in SOT26 packages and have high ESD surge capability and low capacitance.

Applications

Typically used at high-speed ports such as USB 2.0, IEEE1394 (FireWire[®], iLink™), Serial ATA, DVI, HDMI and PCI.

Features

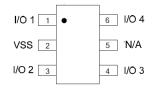
- Clamping Voltage: 7.5V at 12A 100ns, TLP 6V at 5A 8µs/20µs
- IEC 61000-4-2 (ESD): Air 30kV, Contact 30kV
- IEC 61000-4-4 (EFT): 80A (5/50ns)
- IEC 61000-4-5 (Lighting): 20A (8/20µs)
- 4 Channels of ESD Protection
- Low Channel Input Capacitance of 2.4pF Typical
- TLP Dynamic Resistance: 0.15Ω
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

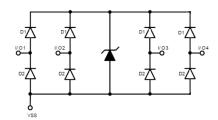
- Case: SOT26
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Schematic
- Terminals Finish Matte Tin Pleated Leadframe.
 Solderable per MIL-STD-202, Method 208 [®]
- Weight: 0.016 grams (Approximate)







Device Schematic



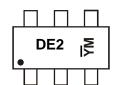
Ordering Information (Note 4)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
D5V0P4URL6SO-7	Standard	DE2	7	8	3,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



DE2 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: A = 2015) M = Month (ex: 9 = September) Note: "—" Represents Internal Code

Date Code Key

Year	20	15	20	16	20	17	20	18	20	19	20	20
Code	())	Е		F		(3	ŀ	1
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Current, per IEC 61000-4-5	I _{PP}	20	Α	I/O to V _{SS} , 8/20µs
Peak Pulse Power, per IEC 61000-4-5	P _{PP}	180	W	I/O to V _{SS} , 8/20µs
ESD Protection – Contact Discharge, per IEC 61000-4-2	V _{ESD_CONTACT}	30	kV	I/O to V _{SS}
ESD Protection – Air Discharge, per IEC 61000-4-2	V_{ESD_AIR}	30	kV	I/O to V _{SS}
Operating Temperature	T _{OP}	-55 to +85	°C	_
Storage Temperature	T _{STG}	-55 to +150	°C	_

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation Typical (Note 5)	P _D	300	mW
Thermal Resistance, Junction to Ambient Typical (Note 5)	$R_{\theta JA}$	417	°C/W

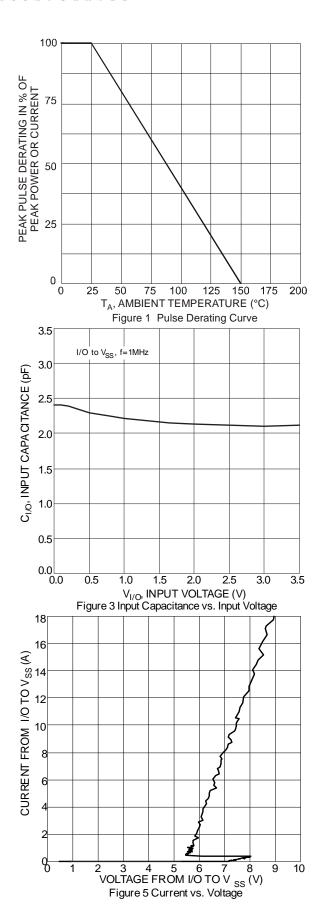
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

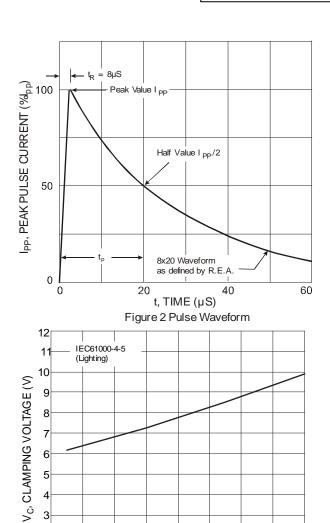
Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Working Voltage	V_{RWM}		_	3.3	V	I _R =1mA, I/O to V _{SS}
Reverse Current (Note 6)	I _R	_	_	1	μΑ	$V_R = 3.3V$, I/O to V_{SS}
Reverse Breakdown Voltage	V_{BR}	4.5	_	8.0	V	$I_R = 1 \text{mA}$, I/O to V_{SS}
Forward Clamping Voltage	V _F	_	0.8	1.2	V	$I_F = 15\text{mA}$, V_{SS} to I/O
Reverse Clamping Voltage (Note7)	Vc	_	6	_	V	$I_{PP} = 5A$, I/O to V_{SS} , 8/20 μ s
ESD Clamping Voltage	V _{ESD}	_	7.5	_	V	TLP, 12A, $t_P = 100$ ns, I/O to V_{SS}
Dynamic Reverse Resistance	R _{DIF-R}	_	0.15	_	Ω	TLP, 12A, t_P = 100ns, I/O to V_{SS}
Channel Input Capacitance	C _{I/O}	_	2.4	3	pF	$V_{I/O} = 1.65V, V_{SS} = 0V, f = 1MHz$
Delta C _{I/O}	C _{I/OMAX} -C _{I/OMIN}	_	0.04	_	pF	C _{I/OMAX} —C _{I/OMIN}

Notes:

- 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. website at http://www.diodes.com/package-outlines.html.
- 6. Short duration pulse test used to minimize self-heating effect.
- 7. Clamping voltage value is based on an 8x20µs peak pulse current (IPP) waveform.





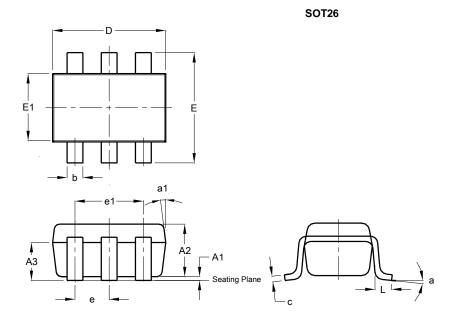


5
3
2
1
04
6
8
10
12
14
16
18
20
CURRENT FROM I/O TO V_{SS} (A)
Figure 4 Clamping Voltage Characteristic



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

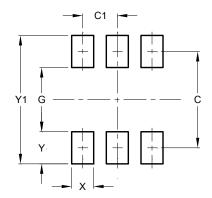


SOT26						
Dim	Min	Max	Тур			
A 1	0.013	0.10	0.05			
A2	1.00	1.30	1.10			
А3	0.70	0.80	0.75			
b	0.35	0.50	0.38			
С	0.10	0.20	0.15			
D	2.90	3.10	3.00			
е	_	_	0.95			
e1	_	_	1.90			
E	2.70	3.00	2.80			
E1	1.50	1.70	1.60			
L	0.35	0.55	0.40			
а	_	_	8°			
a1	_	_	7°			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT26



Dimensions	Value (in mm)
С	2.40
C1	0.95
G	1.60
Х	0.55
Y	0.80
Y1	3.20



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