

CR04AM-12A

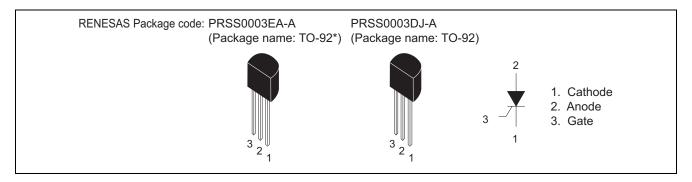
600V - 0.4A - Thyristor Low Power Use R07DS0636EJ0300 Rev.3.00 Aug 25, 2015

Features

$$\begin{split} \bullet & \quad I_{T \, (AV)} : 0.4 \; A \\ \bullet & \quad V_{DRM} : 600 \; V \\ \bullet & \quad I_{GT} : 100 \; \mu A \\ \bullet & \quad RoHS \; Compliant \end{split}$$

- Non-Insulated Type
- Planar Passivation Type
- Halogen-free package (PRSS0003DJ-A)
- Completely Pb-free package (PRSS0003DJ-A)

Outline



Applications

Solid state relay, igniter, strobe flasher, circuit breaker, and general purpose control applications

Maximum Ratings

Parameter	Symbol	Voltage class	Unit	
Faranietei	Symbol	12	Onit	
Repetitive peak reverse voltage	V _{RRM}	600	V	
Non-repetitive peak reverse voltage	V _{RSM}	720	V	
DC reverse voltage	V _{R(DC)}	480	٧	
Repetitive peak off-state voltage Note1	V _{DRM}	600	V	
DC off-state voltage Note1	$V_{D(DC)}$	480	V	

Notes: 1. With gate to cathode resistance R_{GK}=1 $k\Omega$

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I _{T(RMS)}	0.63	Α	
Average on-state current	I _{T(AV)}	0.4	Α	Commercial frequency, sine half wave 180° conduction, Ta=54°C
Surge on-state current	Ітѕм	10	А	60Hz sine half wave, 1full cycle, peak value, non-repetitive
I ² t for fusing	l ² t	0.4	A ² s	Value corresponding to 1 cycle of half wave 60Hz, surge on-state current
Peak gate power dissipation	P _{GM}	0.5	W	
Average gate power dissipation	P _{G(AV)}	0.1	W	
Peak gate forward voltage	V_{FGM}	6	V	
Peak gate reverse voltage	V_{RGM}	6	V	
Peak gate forward current	I _{FGM}	0.3	Α	
Junction temperature	Tj	- 40 to +125	°C	
Storage temperature	Tstg	- 40 to +125	°C	
Mass	_	0.23	g	Typical value

Electrical Characteristics

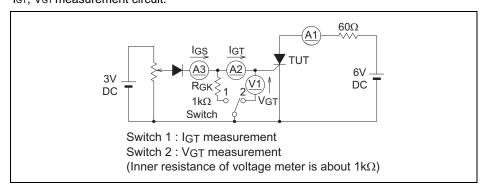
Parameter	Symbol	Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak reverse current	I _{RRM}	_	_	0.5	mA	Tj = 125°C, V _{RRM} applied
Repetitive peak off-state current	I _{DRM}	_	_	0.5	mA	Tj = 125°C, V _{DRM} applied
						R _{GK} =1 kΩ
On-state voltage	V_{TM}	_	_	1.2	V	Tj = 25°C, I _{TM} = 1.2 A
						instantaneous value
Gate trigger voltage	V _G T	_	_	0.8	V	$Tj = 25^{\circ}C, V_D = 6 V,$
						I _T = 0.1 A Note3
Gate non-trigger voltage	V_{GD}	0.2	_	_	V	Tj = 125°C, V _D = 1/2 V _{DRM}
						$R_{GK}=1 K\Omega$
Gate trigger current	I _{GT}	1 Note2	_	100 ^{Note2}	μΑ	$Tj = 25^{\circ}C, V_D = 6 V,$
						I _T = 0.1 A Note3
Holding current	Ін	_	1.5	3	mA	$Tj = 25$ °C, $V_D = 12$ V, $R_{GK} = 1$ kΩ
Thermal resistance	R _{th(j-a)}	_	_	150	°C/W	Junction to ambient

Notes: 2. If special values of I_{GT} are required, choose item D or E from those listed in the table below if possible.

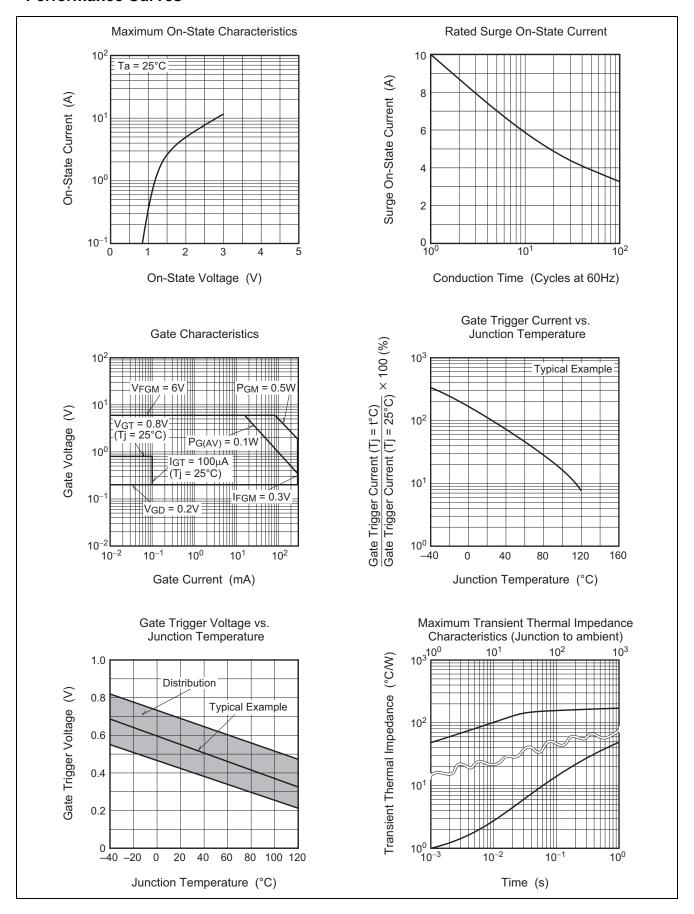
	ltem	Α	В	D	E
Ī	Igτ (μA)	1 to 30	20 to 50	1 to 50	20 to 100

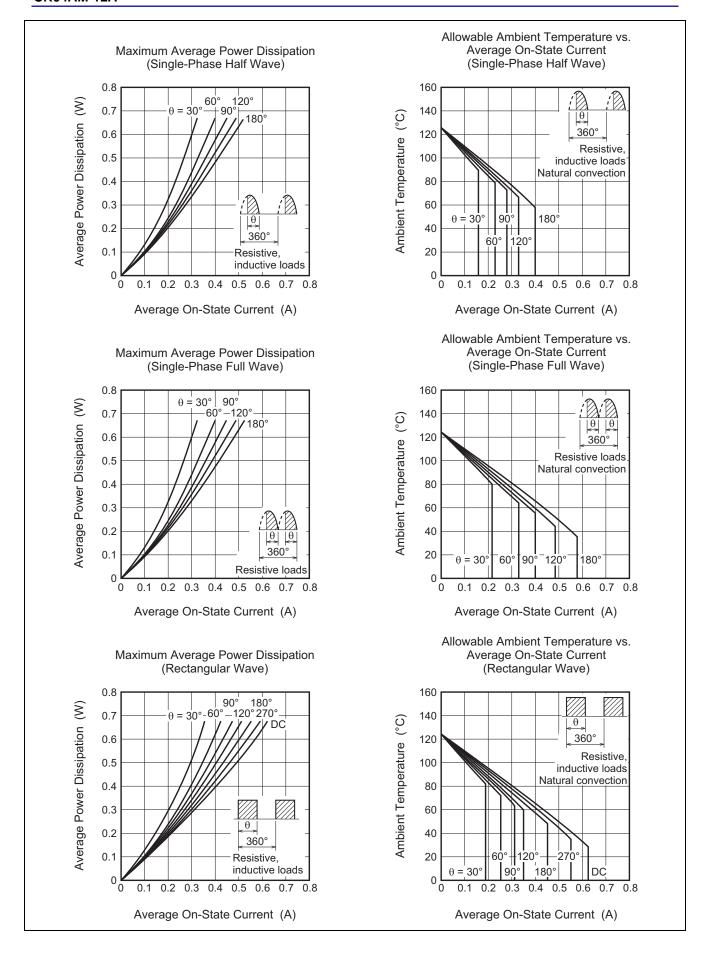
The above values do not include the current flowing through the 1 $k\Omega$ resistance between the gate and cathode.

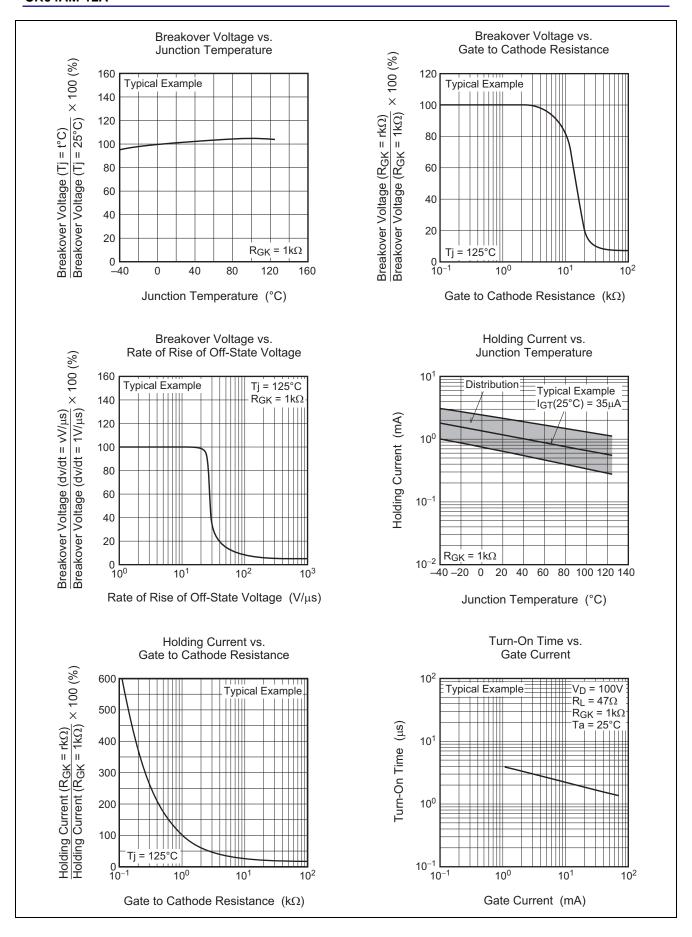
3. Igt, Vgt measurement circuit.

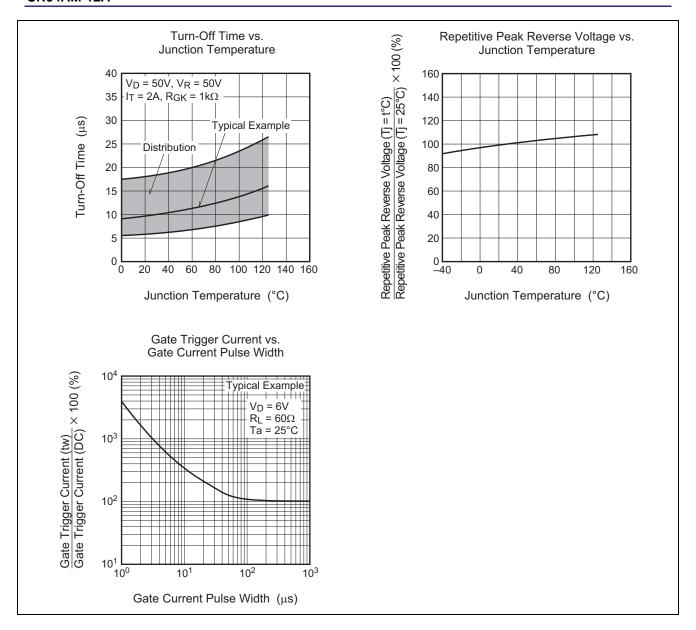


Performance Curves

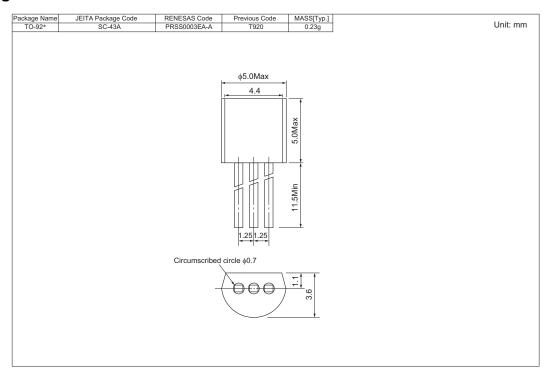






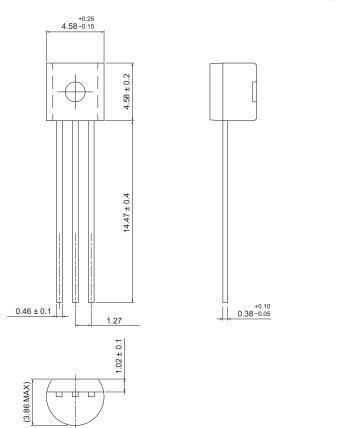


Package Dimensions



JEITA Package Code	RENESAS Code	Previous Code	MASS (Typ) [g]
SC-43A	PRSS0003DJ-A	TO-92	0.23

Unit: mm



Ordering Information

Orderable Part Number	Package	Packing Note	Quantity	Remark
CR04AM-12A#B00	TO-92*	Plastic Bag	500 pcs.	Straight type
CR04AM-12A-□#B00	TO-92*	Plastic Bag	500 pcs.	Straight type, □:I _{GT} item
CR04AM-12A-A6#B00	TO-92*	Plastic Bag	500 pcs.	A6 Lead form
CR04AM-12A-□A6#B00	TO-92*	Plastic Bag	500 pcs.	A6 Lead form, □:I _{GT} item
CR04AM-12A-TB#B00	TO-92*	Adhesive Tape	2000 pcs.	A8 Lead form
CR04AM-12A-□TB#B00	TO-92*	Adhesive Tape	2000 pcs.	A8 Lead form, □:I _{GT} item
CR04AM-12A #BD0	TO-92	Plastic Bag	1000 pcs.	Straight type, Halogen-free
CR04AM-12A-□#BD0	TO-92	Plastic Bag	1000 pcs.	Straight type, Halogen-free, □:I _{GT} item
CR04AM-12A-A6#BD0	TO-92	Plastic Bag	1000 pcs.	A6 Lead form, Halogen-free
CR04AM-12A-□A6#BD0	TO-92	Plastic Bag	1000 pcs.	A6 Lead form, Halogen-free, □:I _{GT} item
CR04AM-12A-TB#BD0	TO-92	Adhesive Tape	2000 pcs.	A8 Lead form, Halogen-free
CR04AM-12A-□TB#BD0	TO-92	Adhesive Tape	2000 pcs.	A8 Lead form, Halogen-free, □:I _{GT} item

Note: Please confirm the specification about the shipping in detail.

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