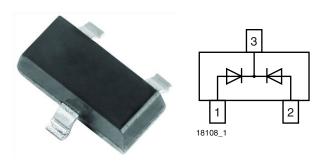


## Vishay Semiconductors

# **Small Signal Switching Diode, Dual**



# FEATURES • Silicon epita

- · Silicon epitaxial planar diode
- · Fast switching dual diode with common cathode
- AEC-Q101 qualified
- Base P/N-E3 RoHS-compliant, commercial grade



- Base P/N-HE3 RoHS-compliant, AEC-Q101 qualified
- Material categorization: For definitions of compliance please see <a href="https://www.vishav.com/doc?99912">www.vishav.com/doc?99912</a>

#### **MECHANICAL DATA**

Case: SOT-23

Weight: approx. 8.8 mg
Packaging codes/options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

PARTS TABLE					
PART	ORDERING CODE	INTERNAL CONSTRUCTION	TYPE MARKING	REMARKS	
BAV23C	BAV23C-E3-08 or BAV23C-E3-18	Dual diodes common cathode	KT6	Tape and reel	
	BAV23C-HE3-08 or BAV23C-HE3-18	Dual diodes common cathode	N10	rape and reer	

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Continuous reverse voltage		$V_{R}$	200	V	
Repetitive peak reverse voltage		$V_{RRM}$	250	V	
Non-repetitive peak forward current	t = 1 μs	I <sub>FSM</sub>	9	Α	
Non-repetitive peak forward surge current	t = 1 s	I <sub>FSM</sub>	0.5	А	
Maximum average forward rectified current (1)		I <sub>F(AV)</sub>	200	mA	
Forward continuous current (2)		I <sub>F</sub>	400	mA	
Repetitive peak forward current		I <sub>FRM</sub>	625	mA	
Power dissipation (2)		P <sub>tot</sub>	350	mW	

#### Notes

- (1) Measured under pulse conditions; pulse time =  $t_p \le 0.3$  ms
- (2) Device on fiberglass substrate

<b>THERMAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Thermal resistance junction to ambient air (1)		R <sub>thJA</sub>	357	K/W	
Junction temperature		Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	- 65 to + 150	°C	
Operating temperature range		T <sub>op</sub>	- 55 to + 150	°C	

#### Note

(1) Device on fiberglass substrate



#### www.vishay.com

## Vishay Semiconductors

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Reverse breakdown voltage	$I_R = 100  \mu A,  t_p = 300  ms$	V <sub>(BR)</sub>	250			V
Forward voltage	I <sub>F</sub> = 100 mA	V <sub>F</sub>			1	V
Forward voltage	I <sub>F</sub> = 200 mA	V <sub>F</sub>			1.25	V
Reverse current	V <sub>R</sub> = 200 V	I <sub>R</sub>			100	nA
neverse current	V <sub>R</sub> = 200 V, T <sub>j</sub> = 150 °C	I <sub>R</sub>			100	μA
Dynamic forward resistance	I <sub>F</sub> = 10 mA	r <sub>f</sub>		5		Ω
Diode capacitance	V <sub>R</sub> = 0 V, f = 1 MHz	C <sub>D</sub>			5	pF
Reverse recovery time	$I_F = I_R = 30$ mA, $R_L = 100 \Omega$ $i_R = 3$ mA	t <sub>rr</sub>			50	ns

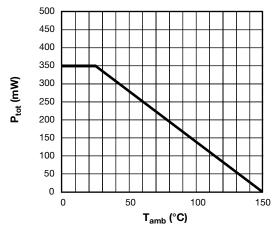
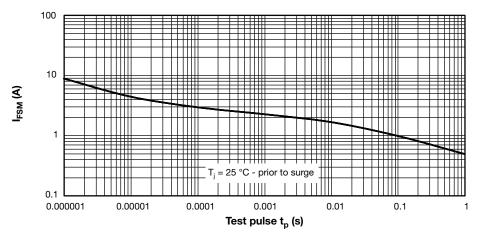


Fig. 1 - P<sub>tot</sub> - Admissible Power Dissipation vs. Ambient Temperature



 $Fig.\ 2 - I_{FSM} - Non-Repetitive\ Peak\ Forward\ Current\ vs.\ Pulse\ Duration\ - \ Maximum\ Admissible\ Values\ of\ Square\ Pulses$ 



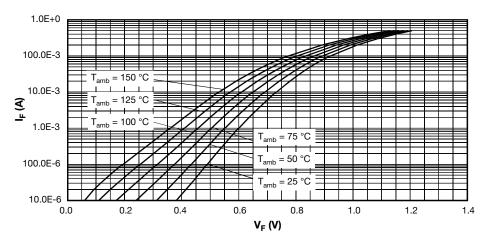


Fig. 3 - V<sub>F</sub> - Typical Forward Current vs. Forward Voltage vs. Various Temperatures

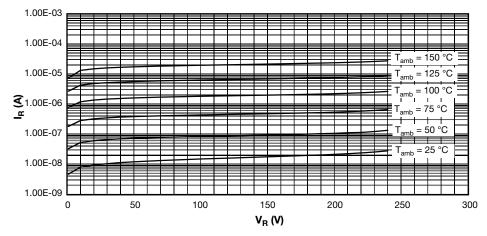
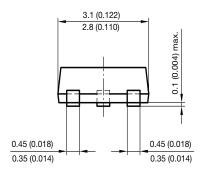


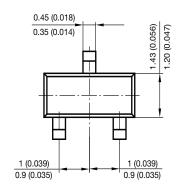
Fig. 4 - I<sub>B</sub> - Typical Reverse Current vs. Reverse Voltage vs. Various Temperatures



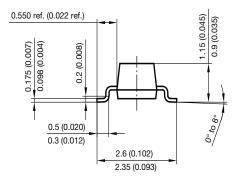


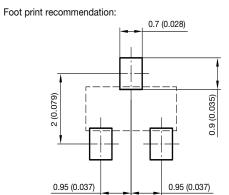
### PACKAGE DIMENSIONS in millimeters (inches): SOT-23





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